

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re U.S. Patent Application of

HAYASHI et al.

Application Number: 09/976,052

Filed: October 15, 2001

For: LEVEL SHIFT CIRCUIT AND SEMICONDUCTOR  
INTEGRATED CIRCUIT

Attorney Docket No. HITA.0110



Unit 2816

Examiner

Nguyen, Minh T

Honorable Assistant Commissioner  
for Patents  
Washington, D.C. 20231

**PETITION FOR EXTENSION OF TIME**

Sir:

Applicants request that the time for taking action in this case be extended pursuant to 37 C.F.R. § 1.136(a) for **one (1) month**. The fee set in 37 C.F.R. § 1.17 for the extension of time is \$110.00.

Please charge any additional fee required for this extension of time to Deposit Account 08-1480. A duplicate copy of this paper is enclosed.

Also filed is a *Response to an Office Action*.

Respectfully submitted,

Stanley P. Fisher  
Registration Number 24,344

Juan Carlos A. Marquez  
Registration Number 34,072

**REED SMITH LLP**  
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August 8, 2003

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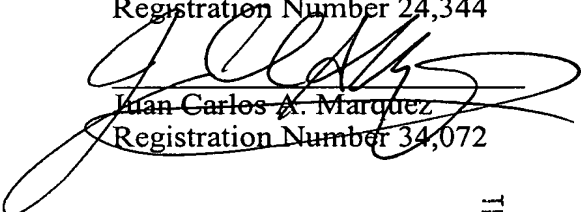
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